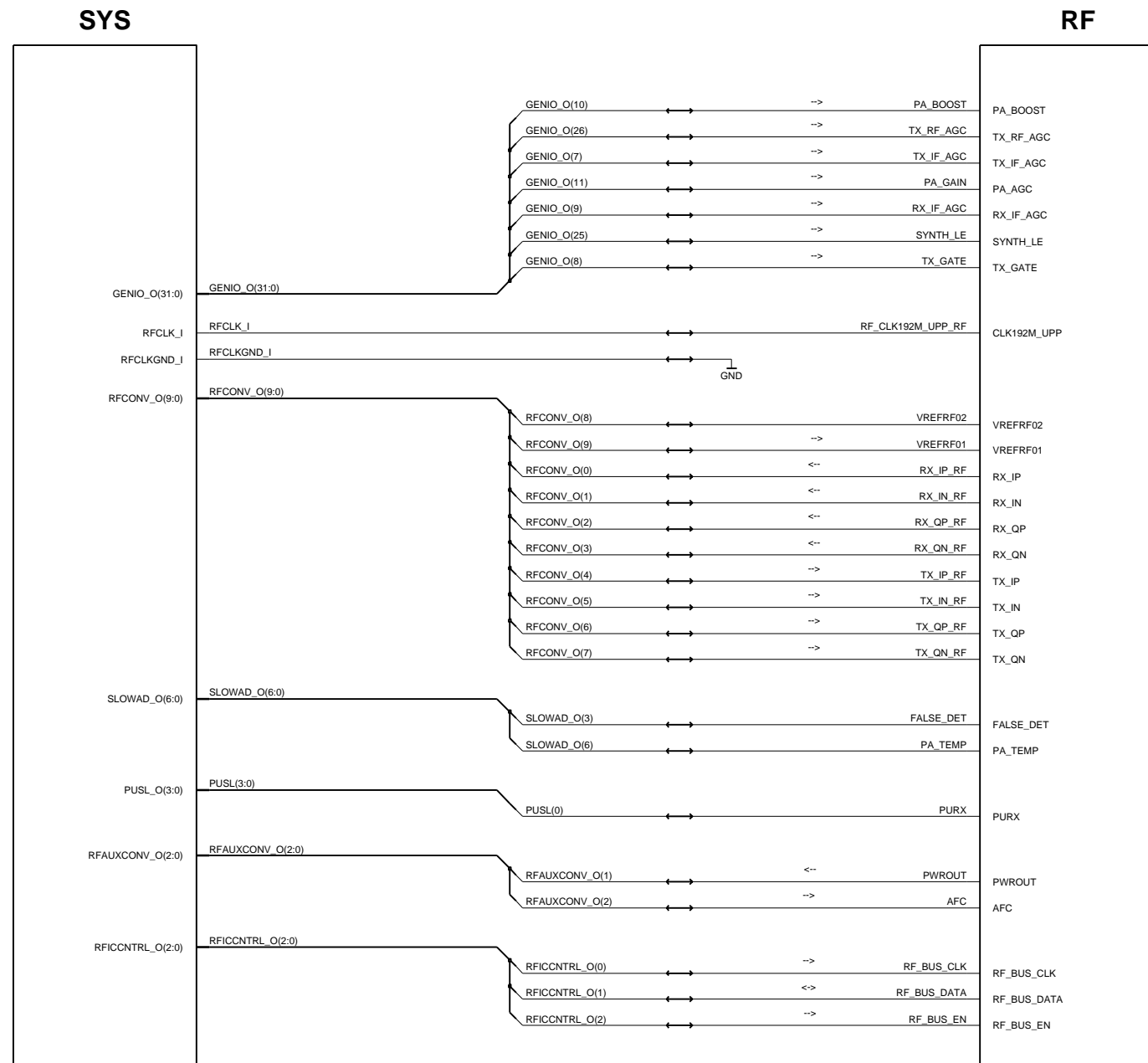
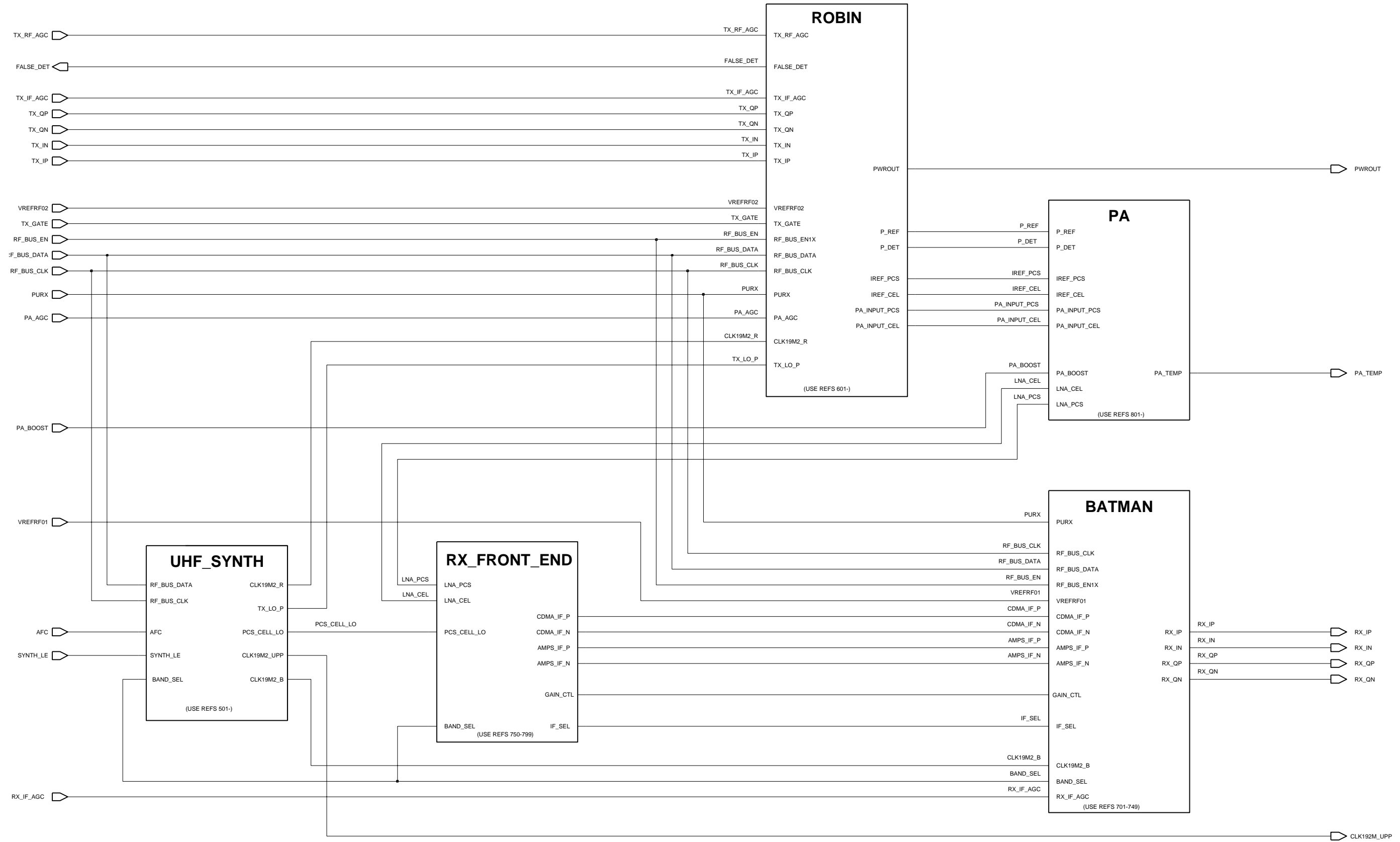


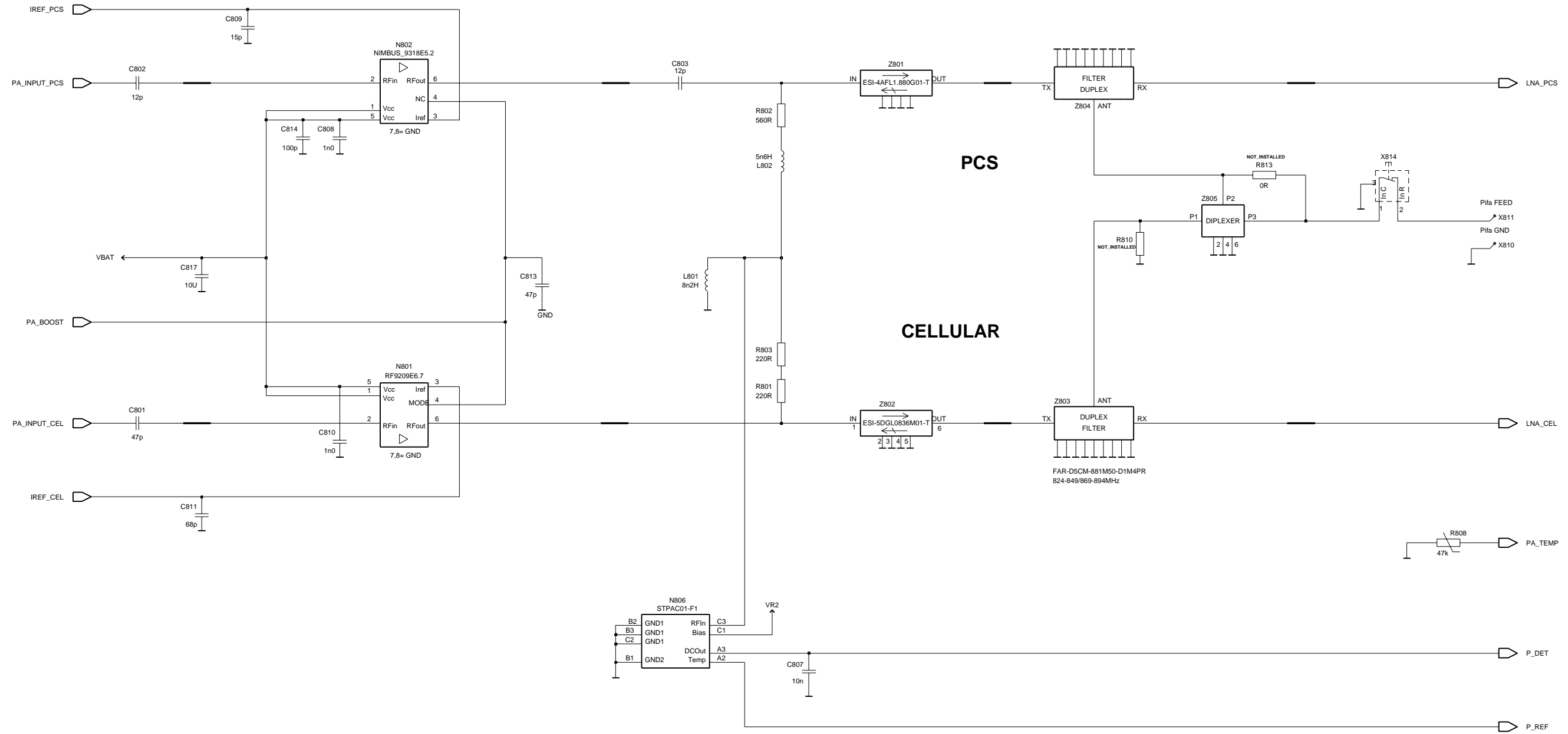
Top-level Diagram



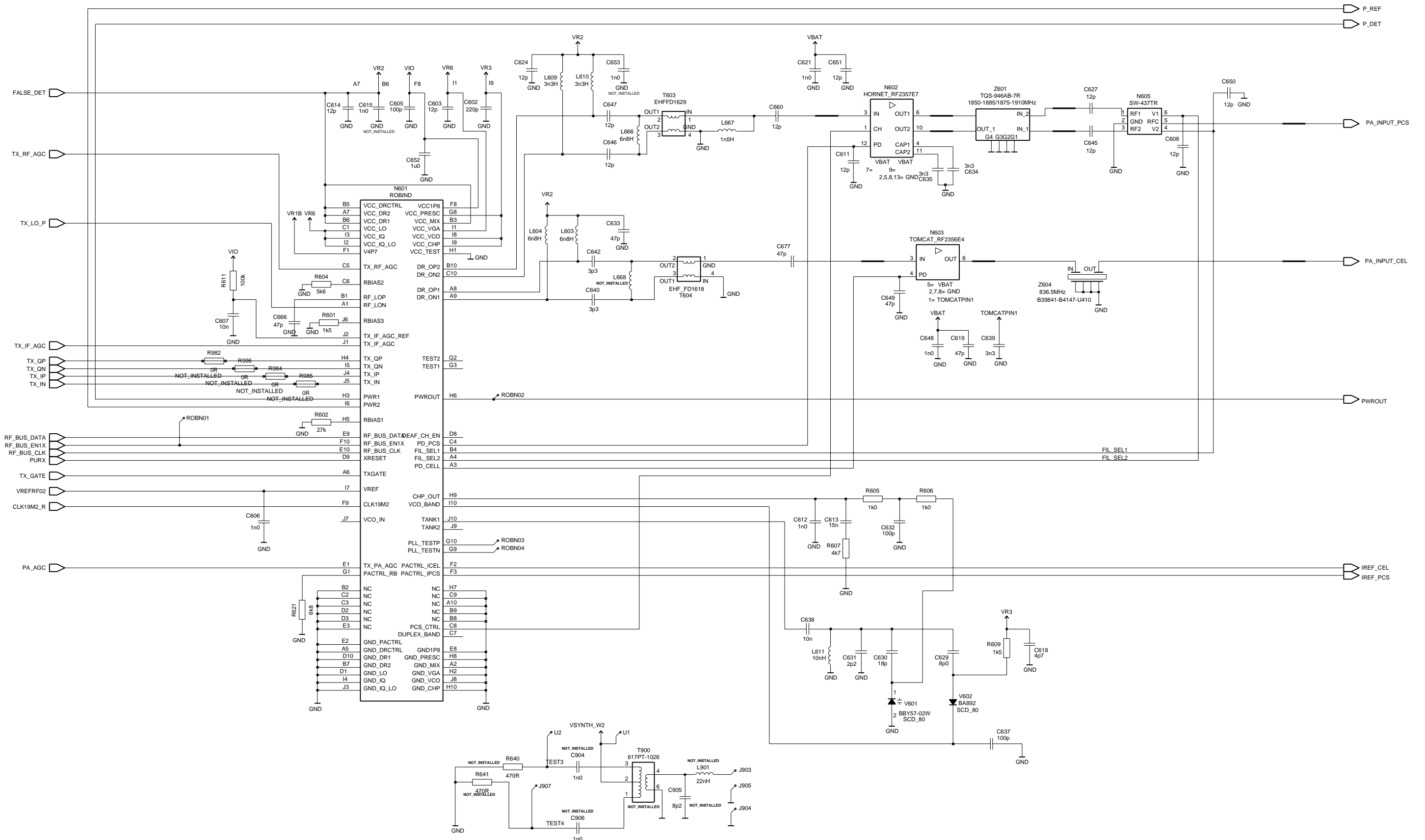
RF Top-level Diagram



PA



Robin



Truth Table for Split-Band Filter PATH:

DRV CNTL	DRV OUT STATE	VCONT1	VCONT2	SWITCH IN STATE
0 volt	RFOUT1	0 volt	2.7 volt	RFI1
2.7 volt	RFOUT2	2.7 volt	0 volt	RFI2

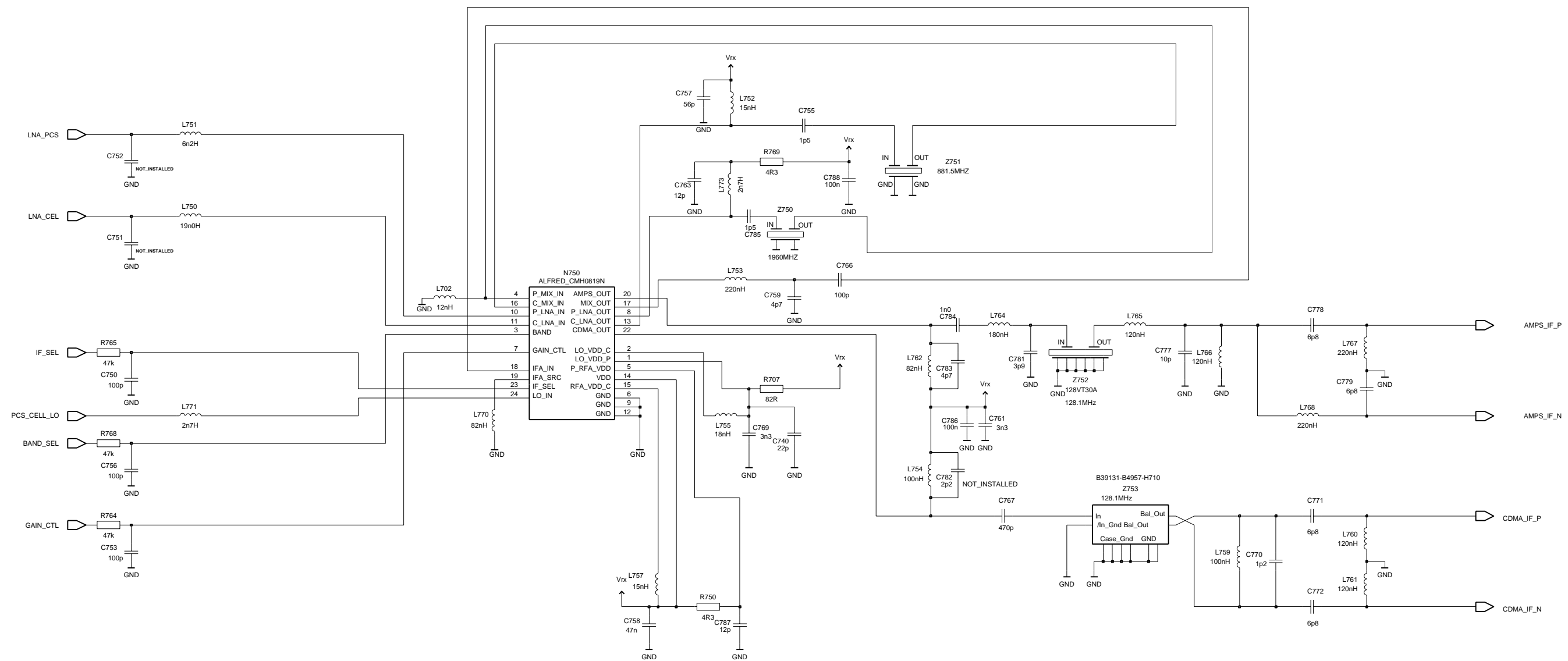
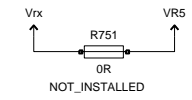
	VCO_BAND
Cell band	low (shorted to ground)
PCS band	tri-stated (open circuit)

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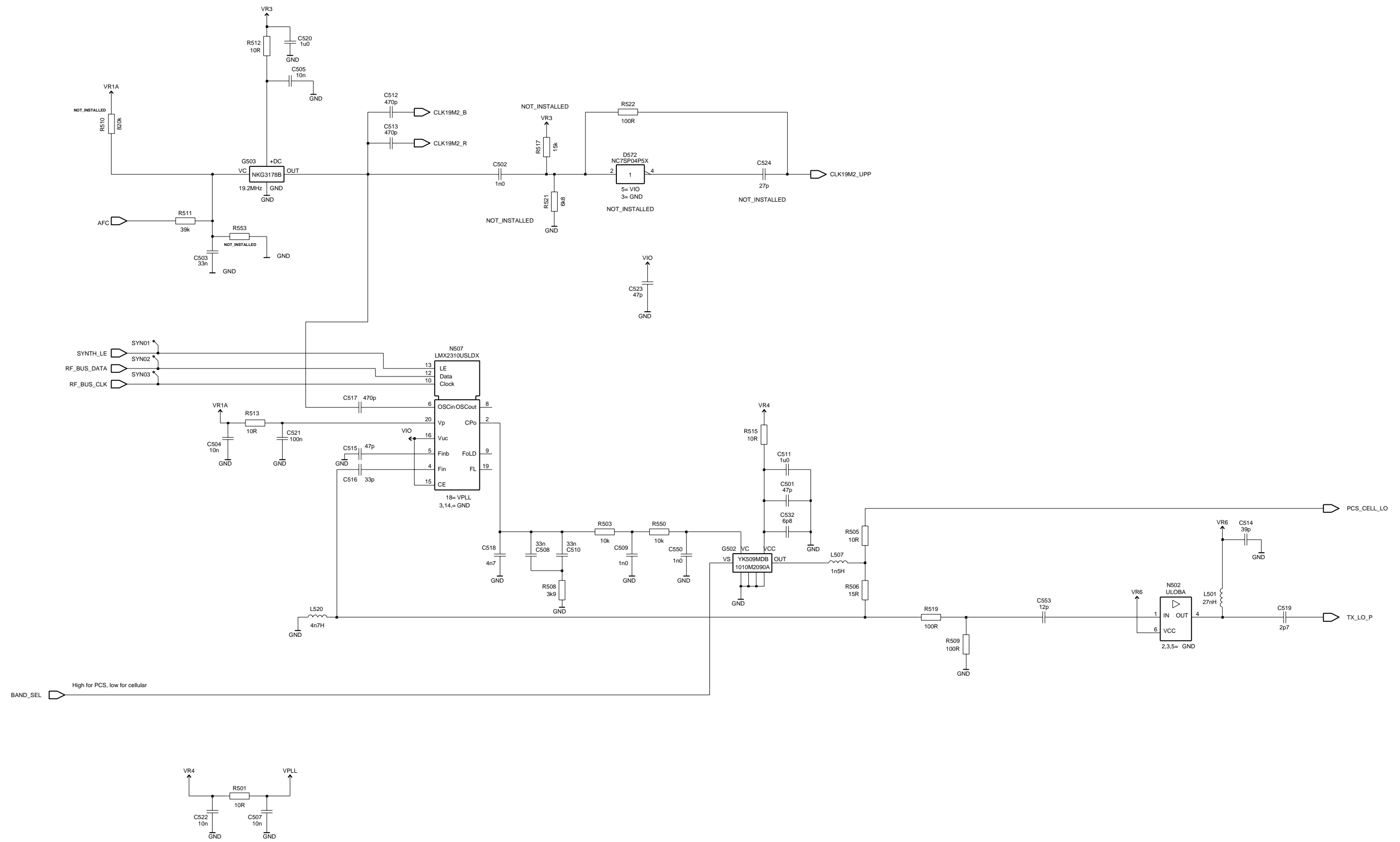
Name	ROBIN		Appr	
Assoc			Des.	
File	\$pcb/hdca3_gui/1bp_ag1_gui_07/RF/robin		Dr.	TUSSEY 16-Oct-
Proj		Drw.	DHB00172-EN	Sheet XX of yy 1 (01)
			Print Yes	V. 7.1 Edit

Front End

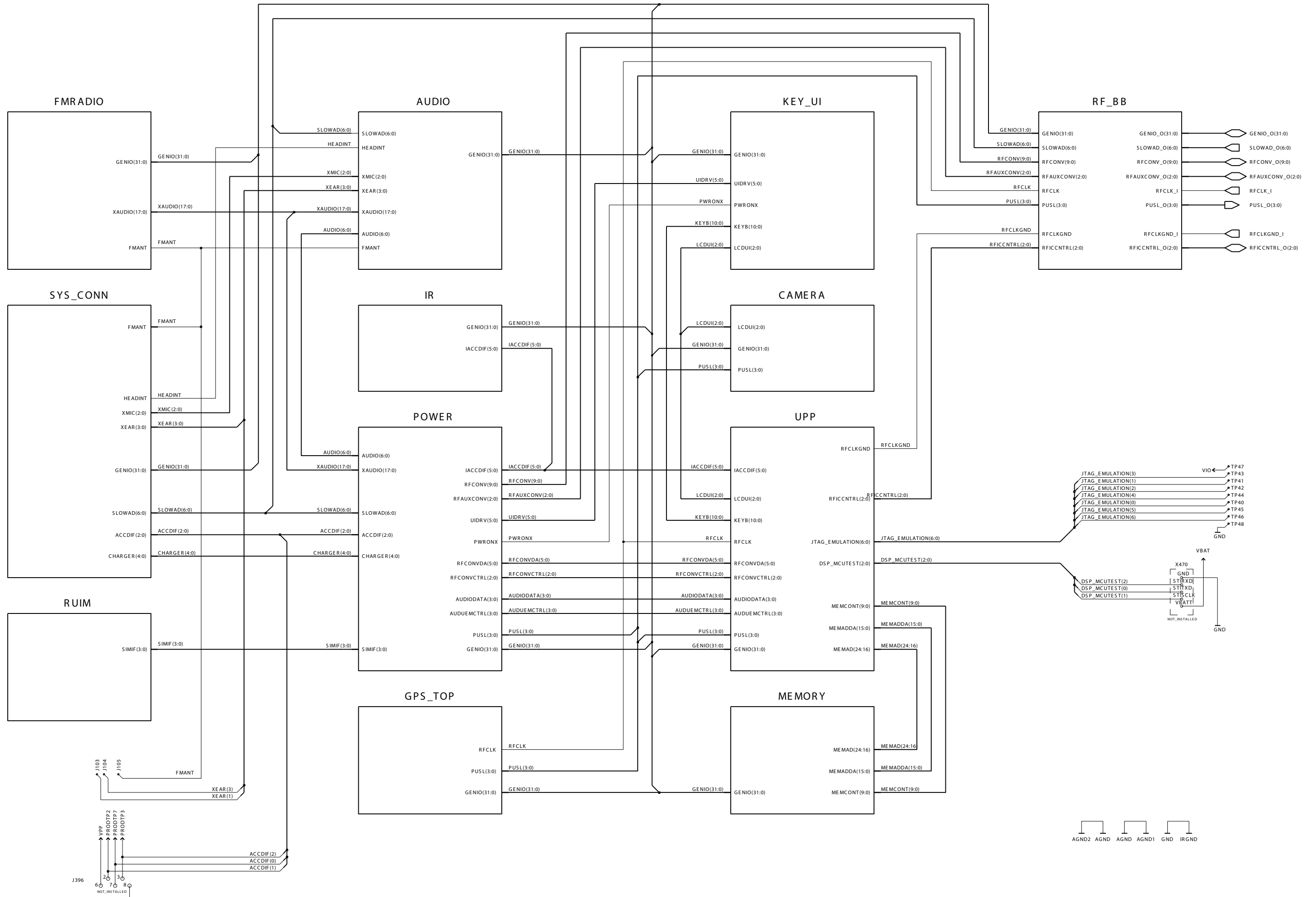


LOGIC INPUTS			
MODES	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

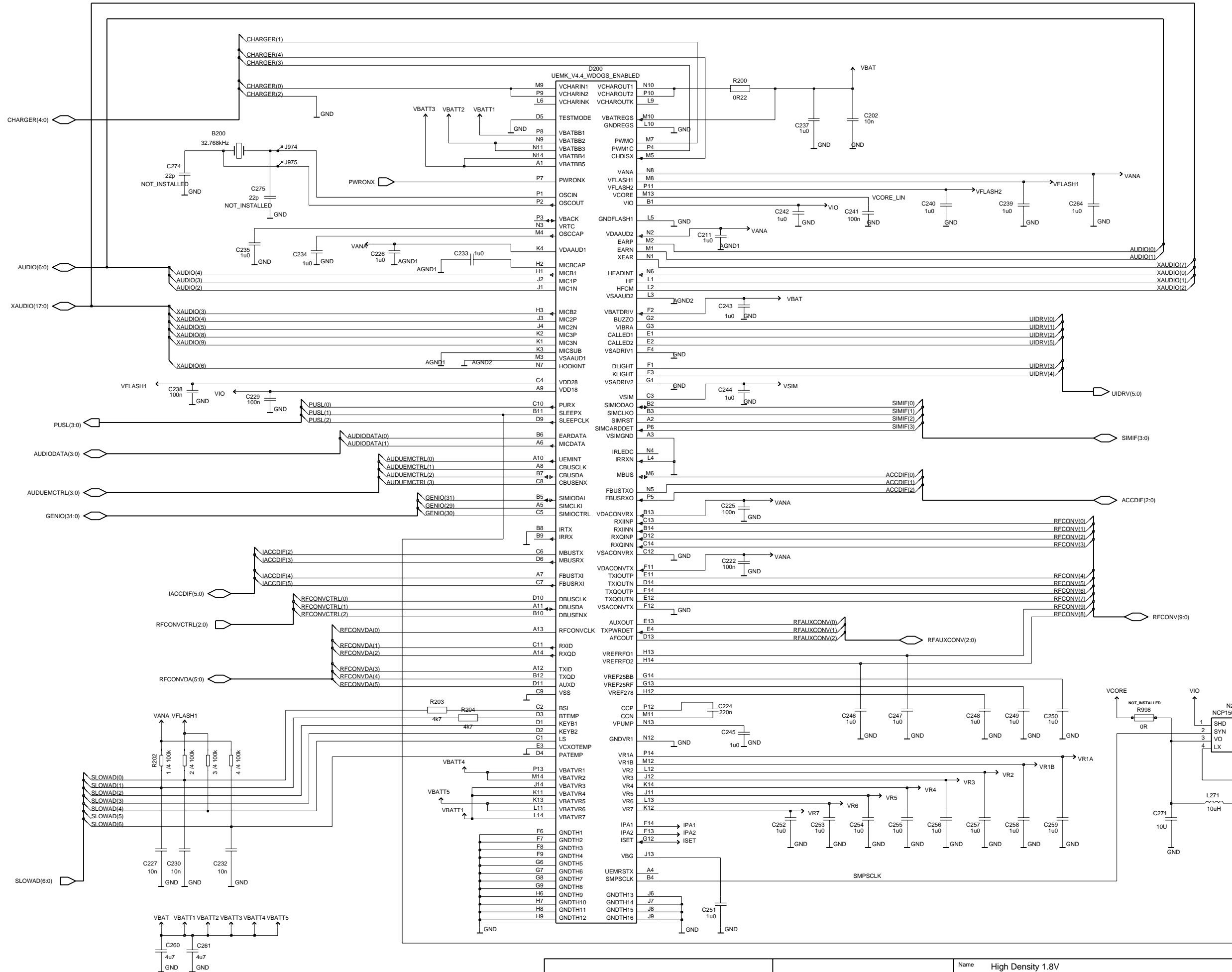
UHF Synthesizer



System Module



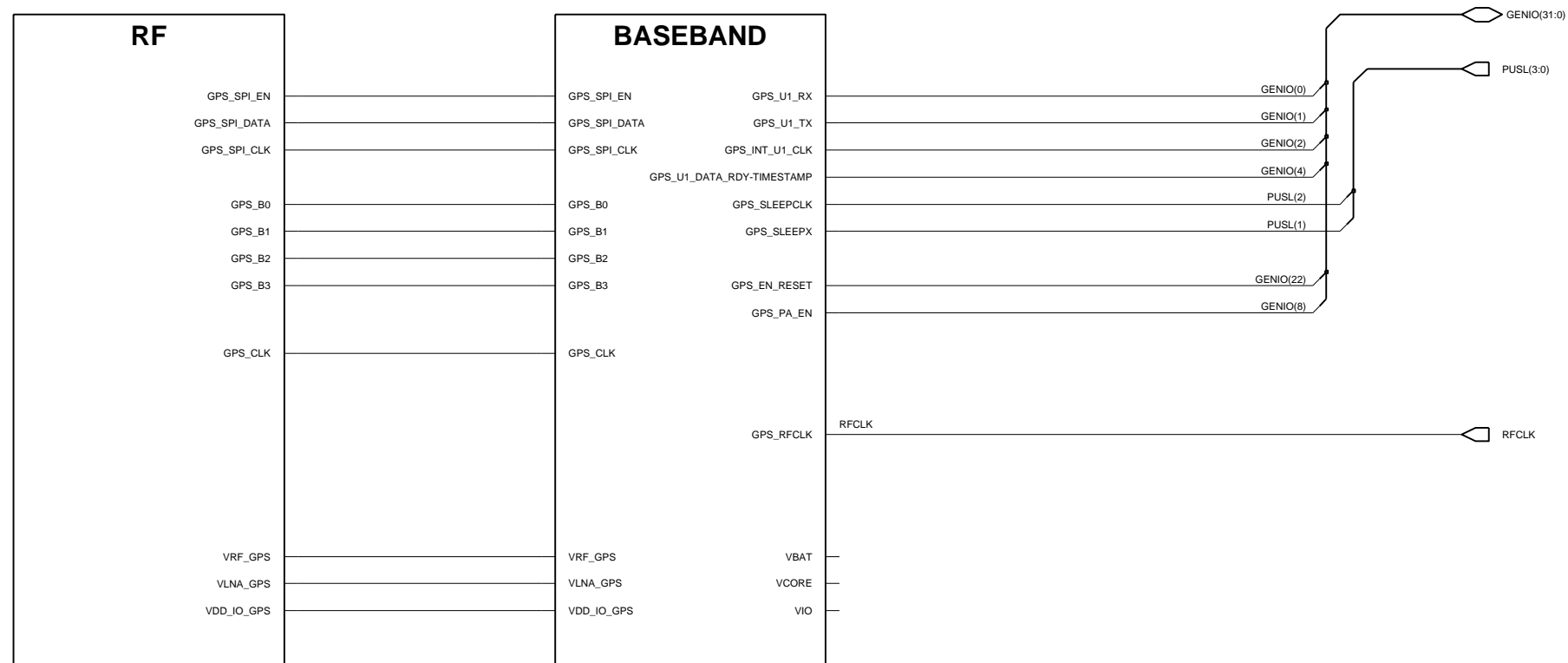
High-Density 1.8V



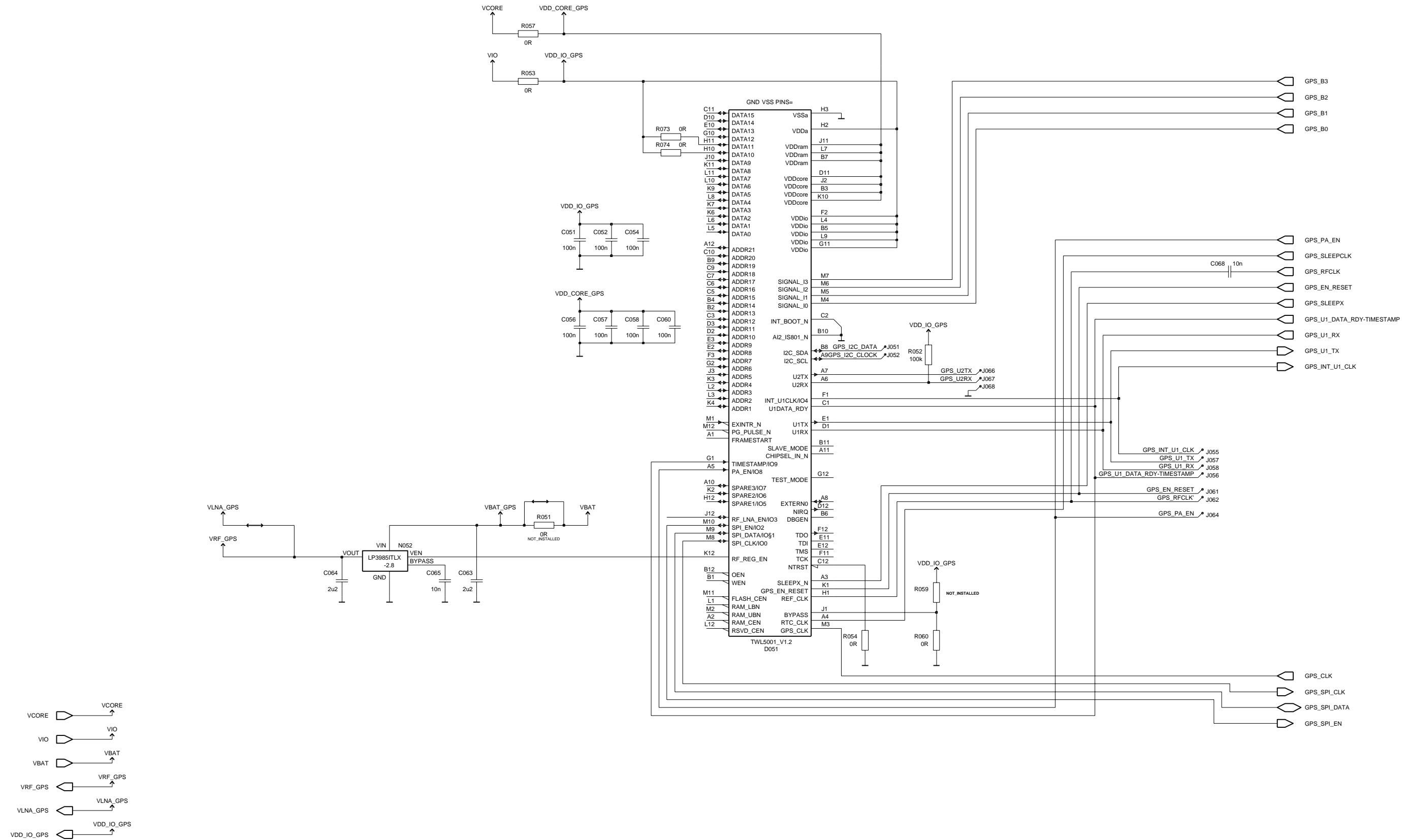
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Name High Density 1.8V

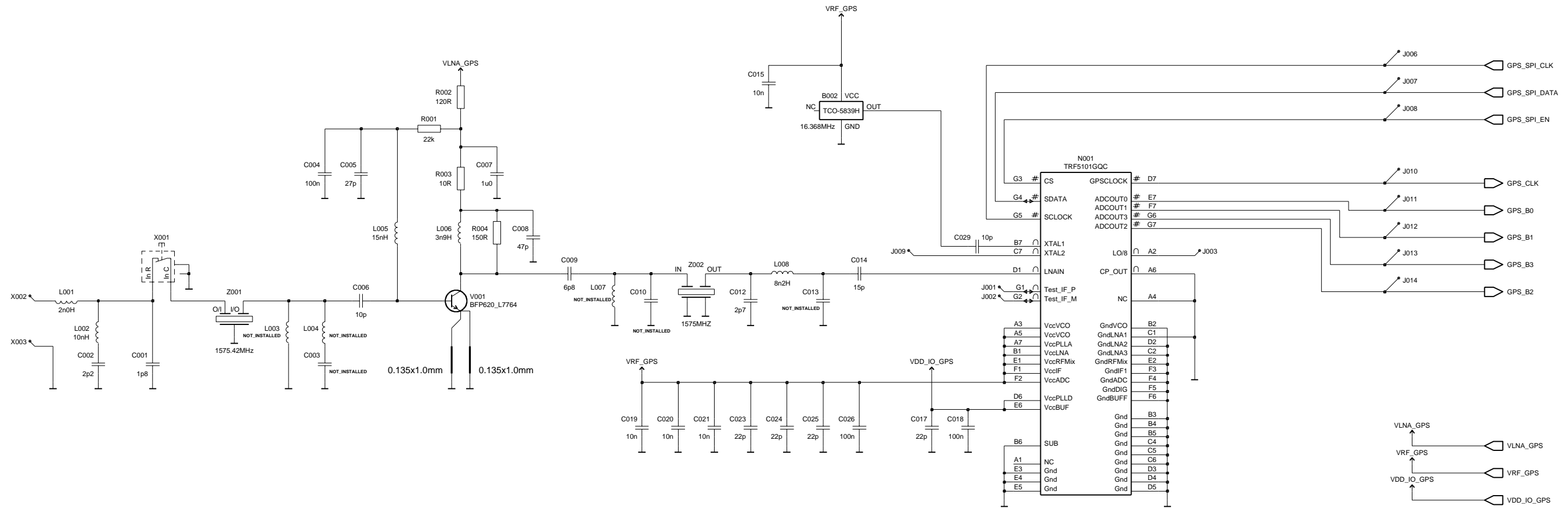
GPS RF-BB Interface



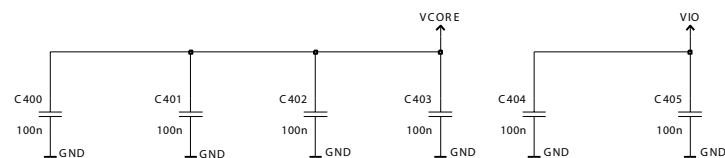
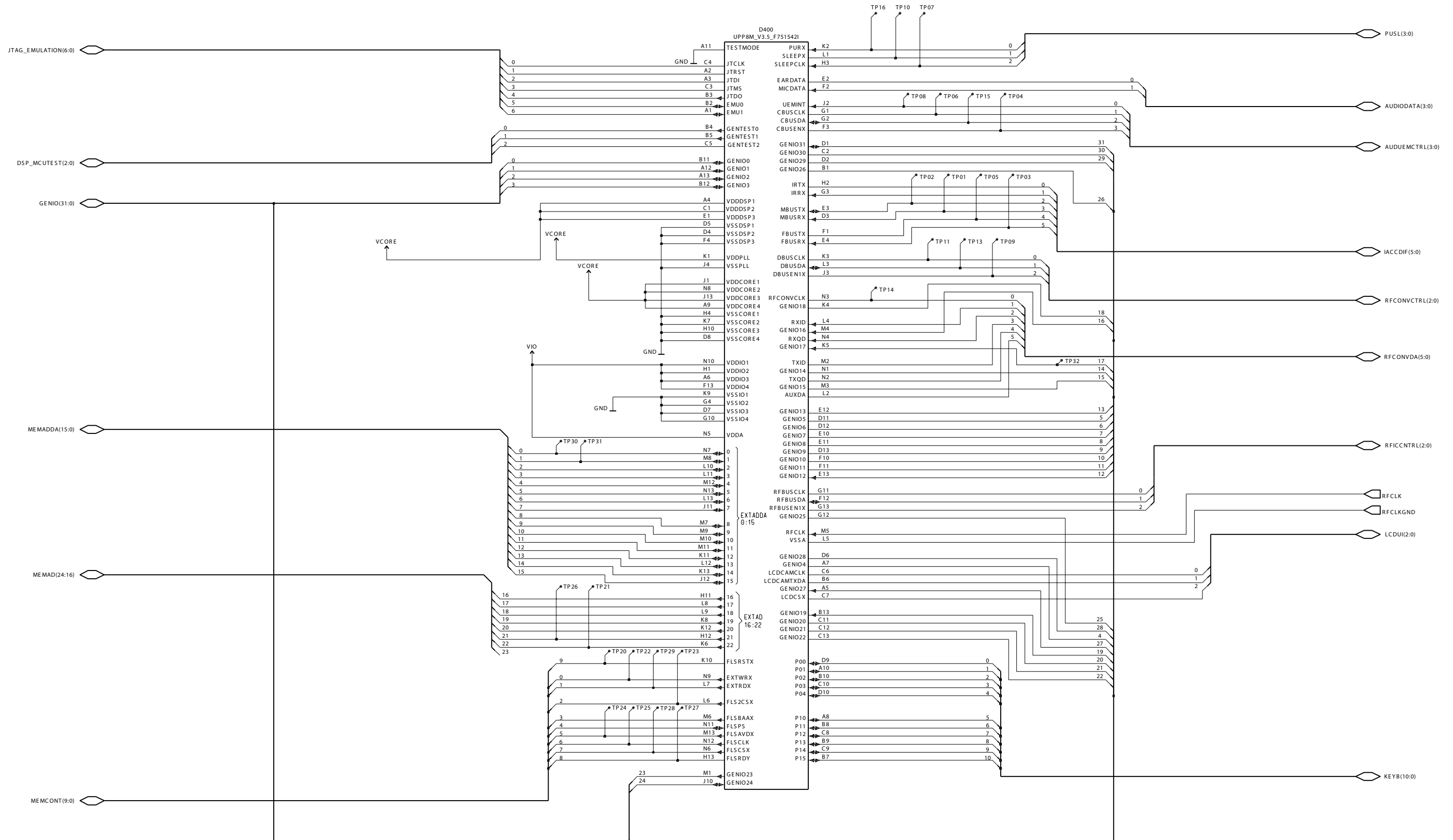
GPS Baseband Circuit Diagram



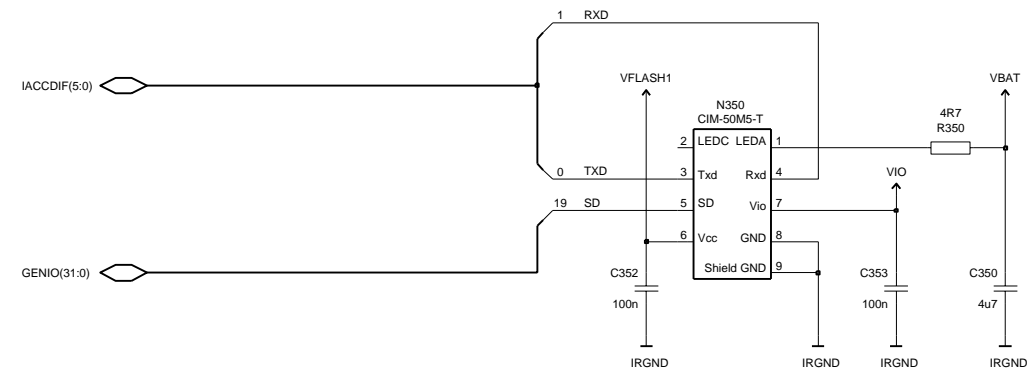
GPS RF Circuit Diagram



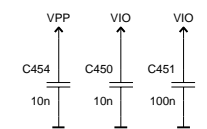
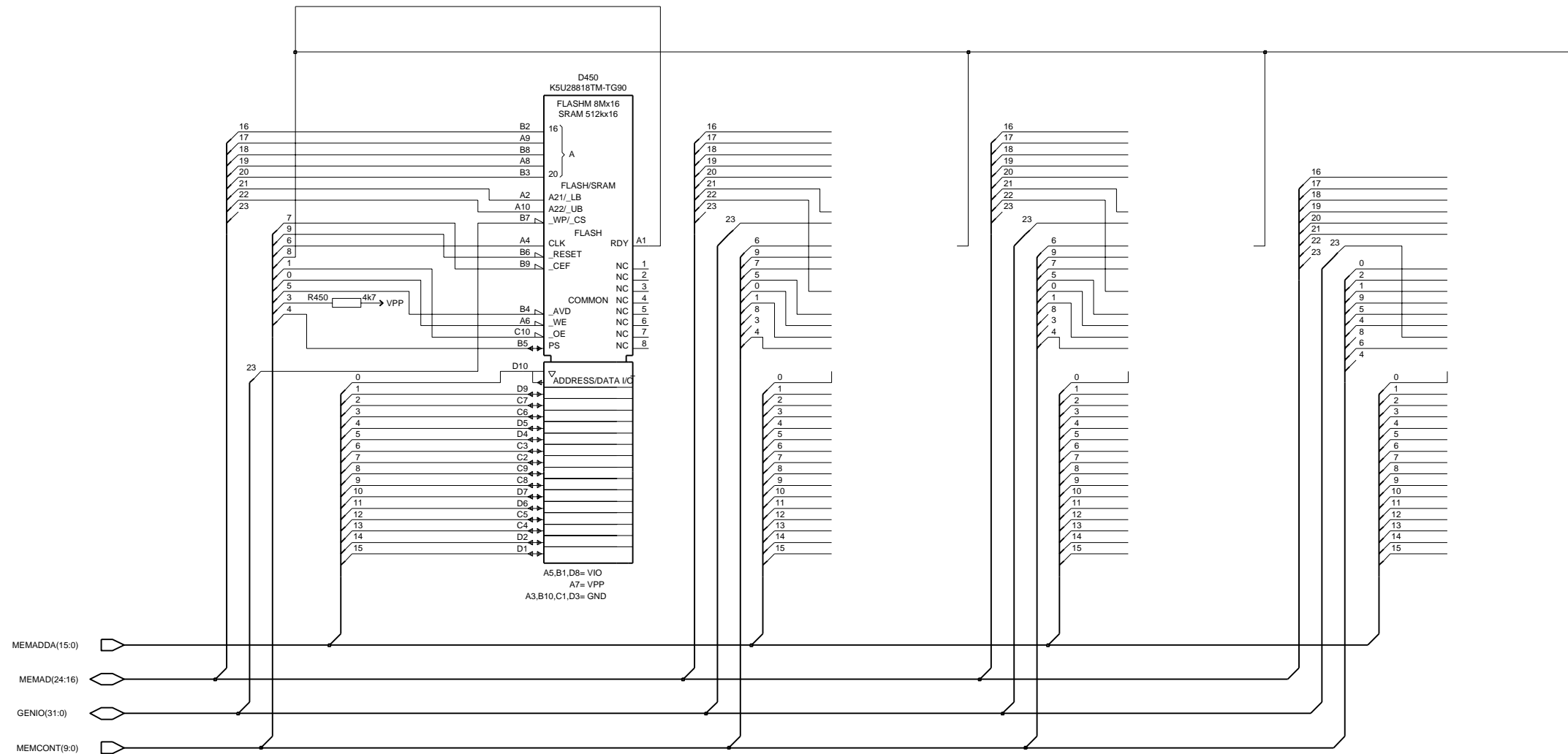
UPP 8M Implementation



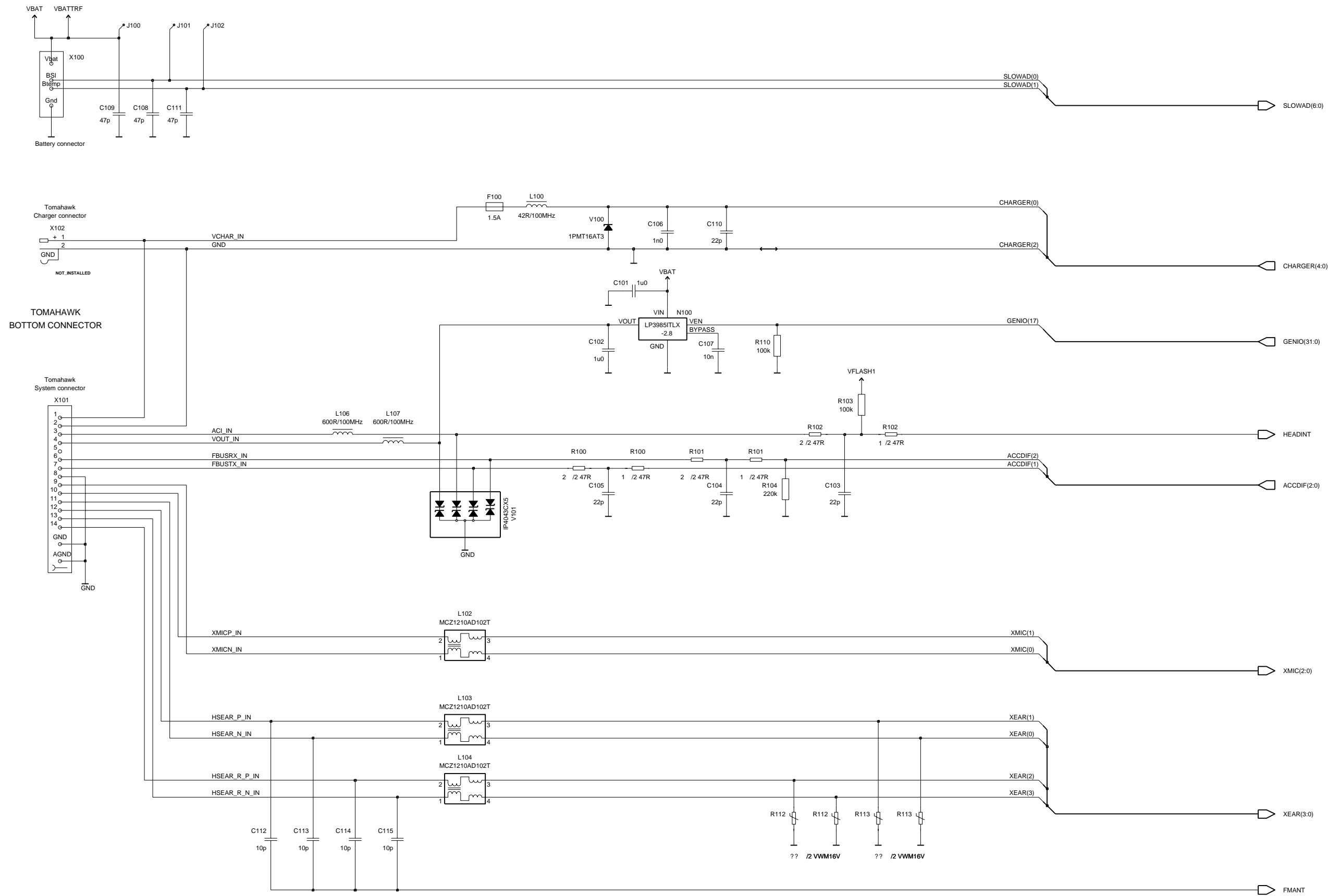
IR Module 1.8V



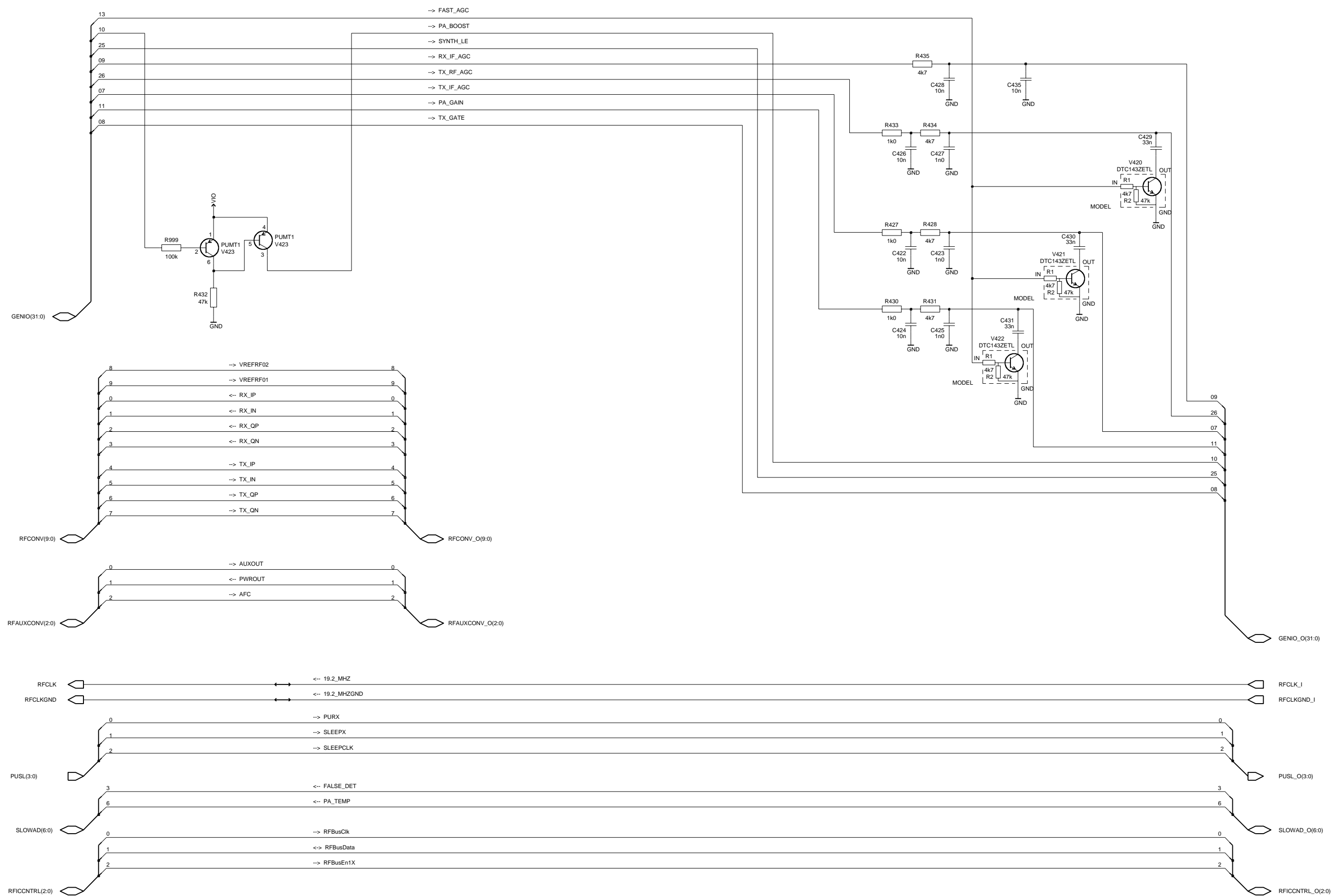
AMD 64-Mbit (Burst Read/Write)



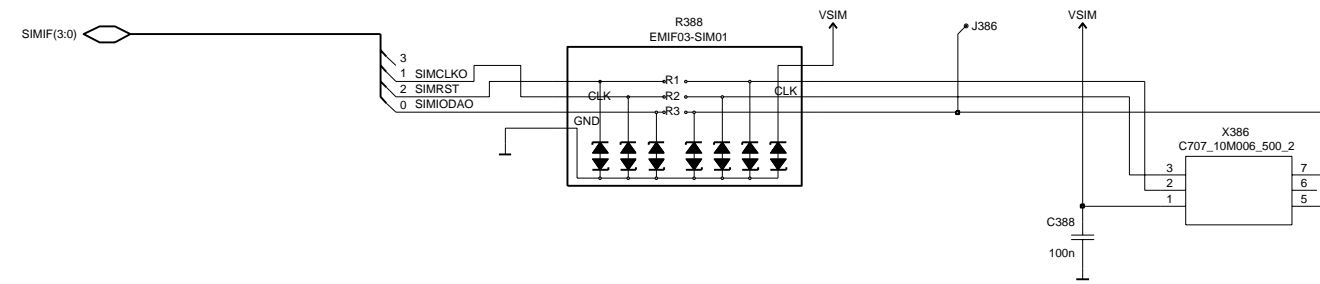
System Connector



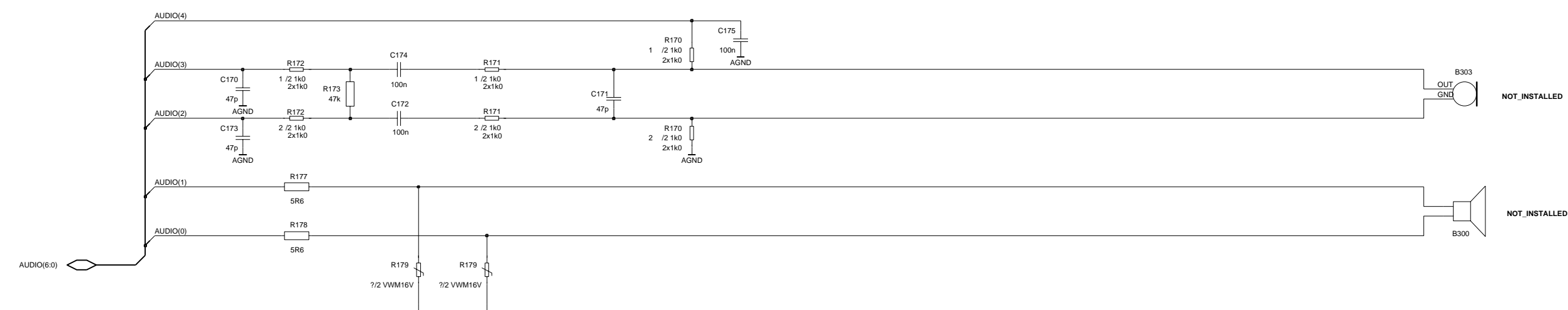
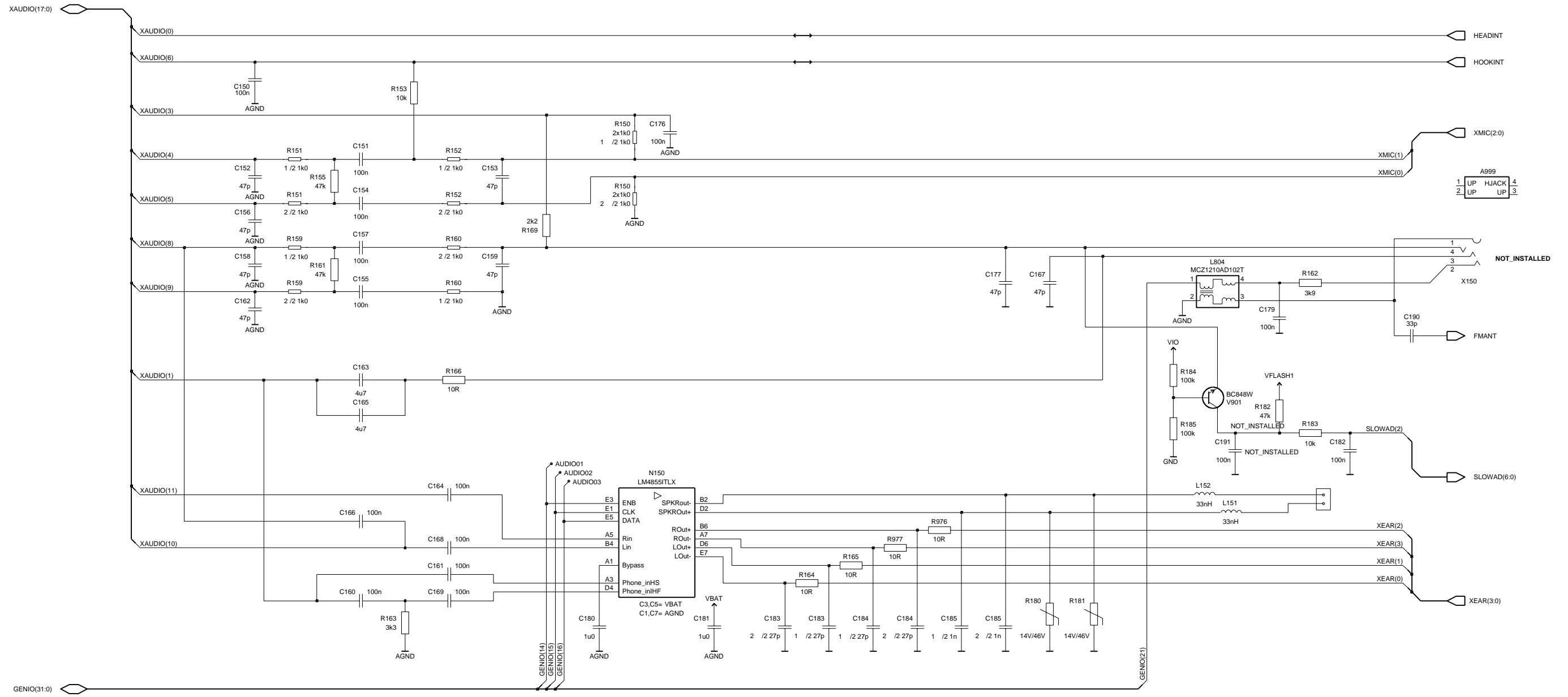
BB-RF Interface



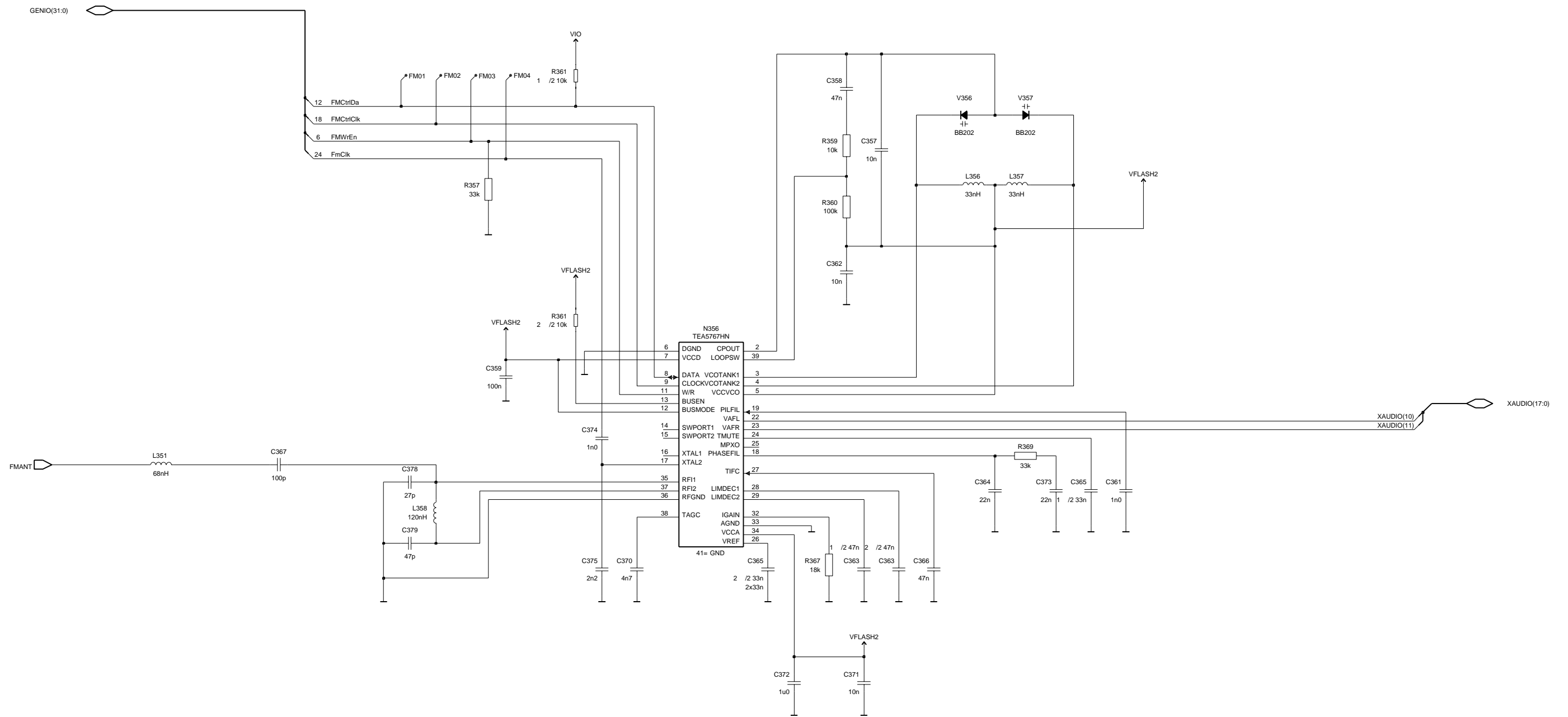
SIM Reader



Audio

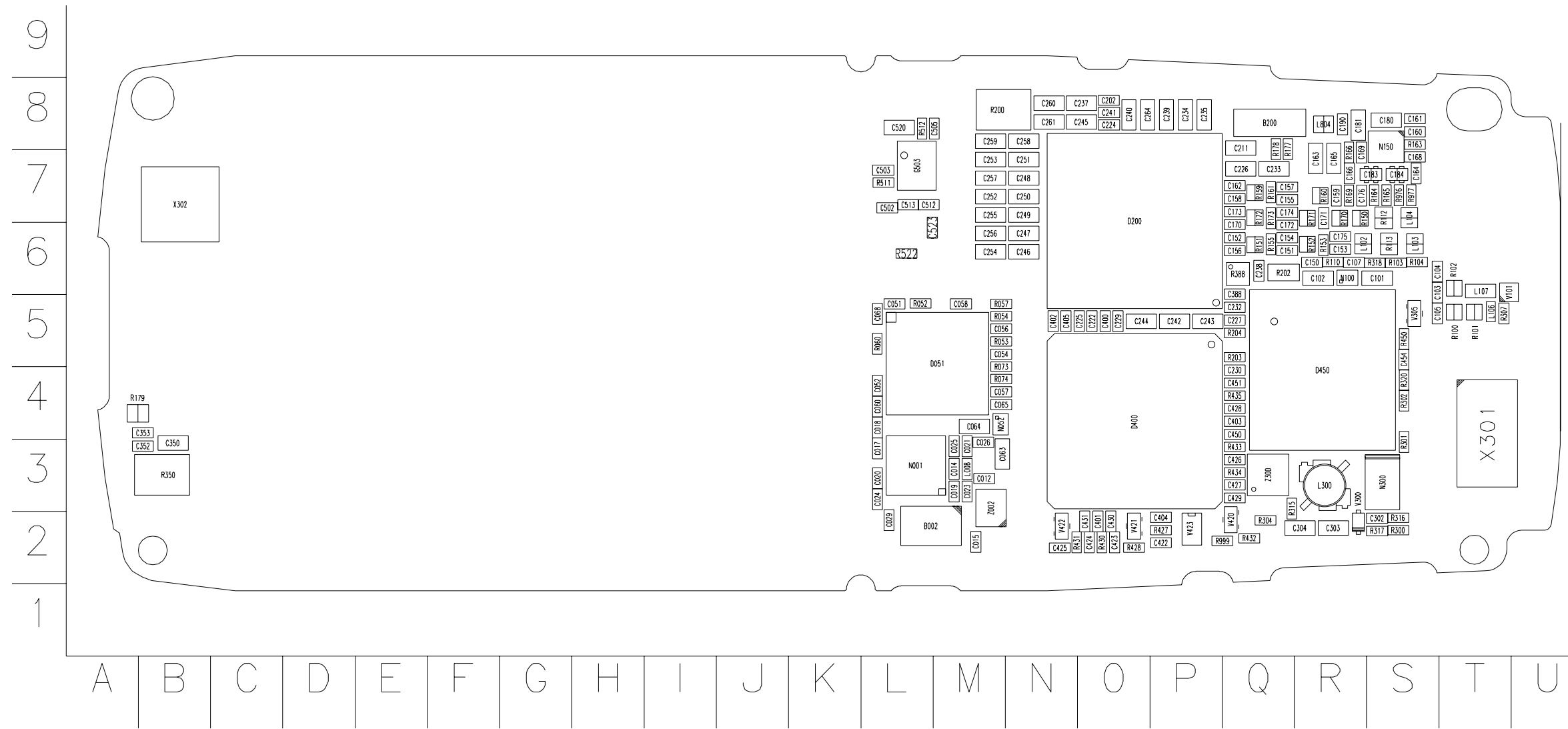


TEA5767 FM Radio Unit



Notice:
 C374 (1n0) and C375 (2n2) are configured for 32kHz reference clock
 If reference clock is 6.5MHz, use C374 (3p9) and C375 (10p)

Component Layout — Top View



Component Layout — Bottom View

